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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Anand Murthy et al.

Serial No.: 10/780,826

Filed: February 17, 2004

For: Fabricating Strained Channel
Epitaxial Source/Drain
Transistors

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Art Unit: 2813

Examiner: David S. Blum

Atty Docket: ITL.1064US
P18031

Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed September 30, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: October 18, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
Cynthia L. Hayden
Cynthia L. Hayden